

Auxiliary Board Address Decoder

External Address Bus

From Sheet 2
Side A

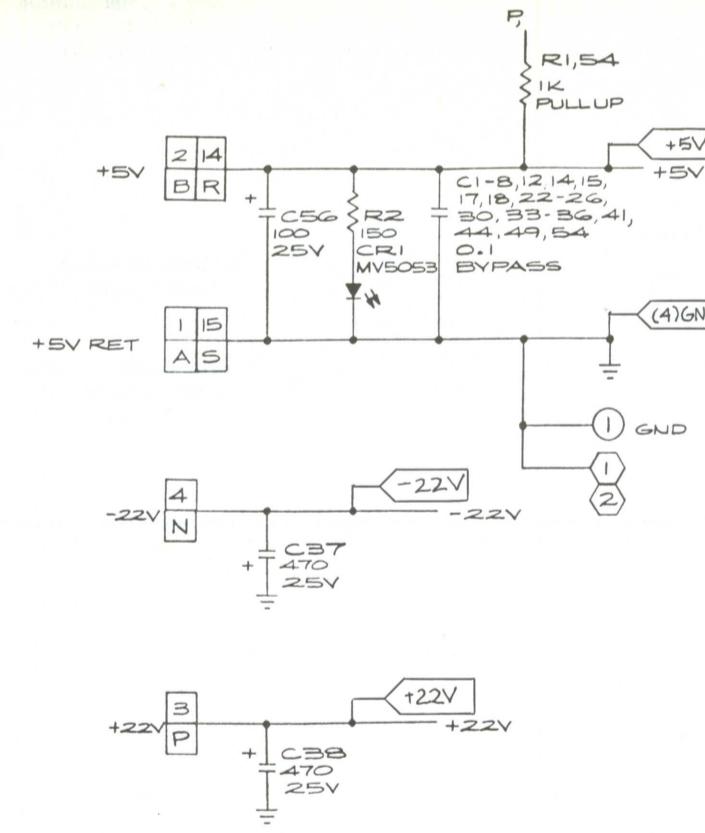
- EAB 7
- EAB 6
- EAB 5
- EAB 4
- EAB 3
- EAB 2
- EAB 1
- EAB 0

External Data Bus

From Sheet 2
Side A

- EDB 7
- EDB 6
- EDB 5
- EDB 4
- EDB 3
- EDB 2
- EDB 1
- EDB 0

Power Inputs



Sheet 3, Side B

Tempest™

High Score Memory

Auxiliary PCB

Audio Output

Control Panel Input

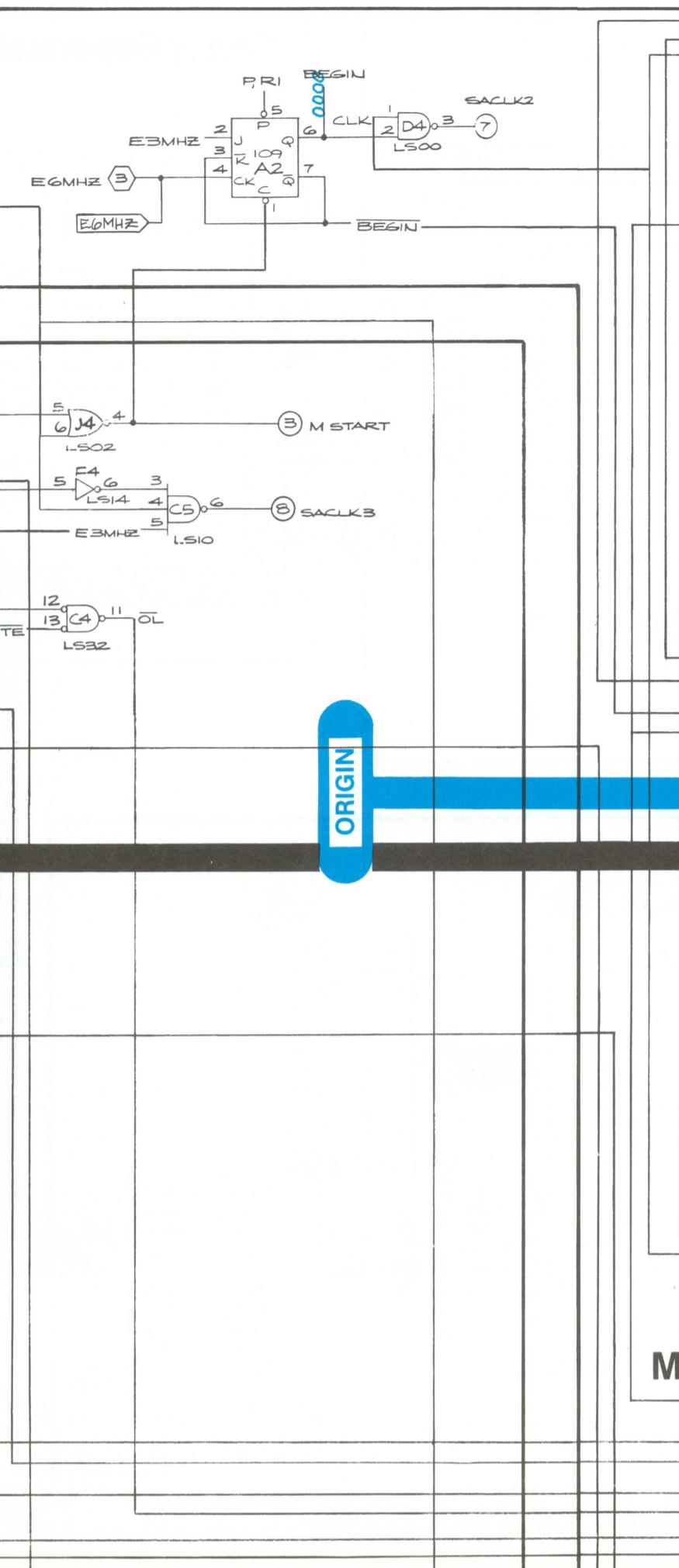
Math Box

Encoder Wheel

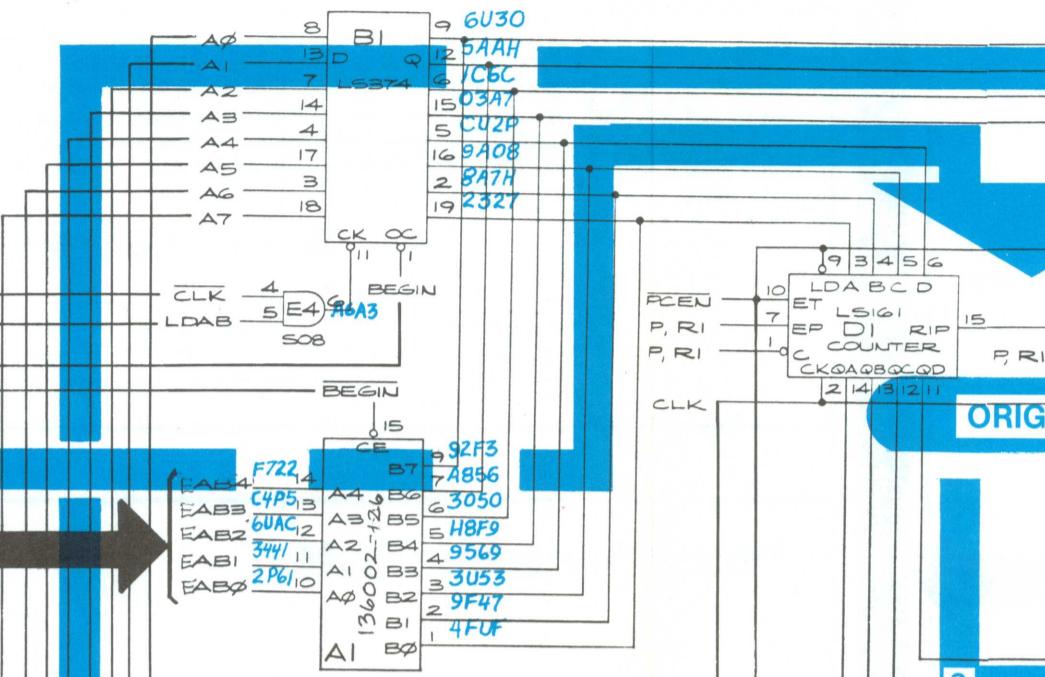
037585-01 B +

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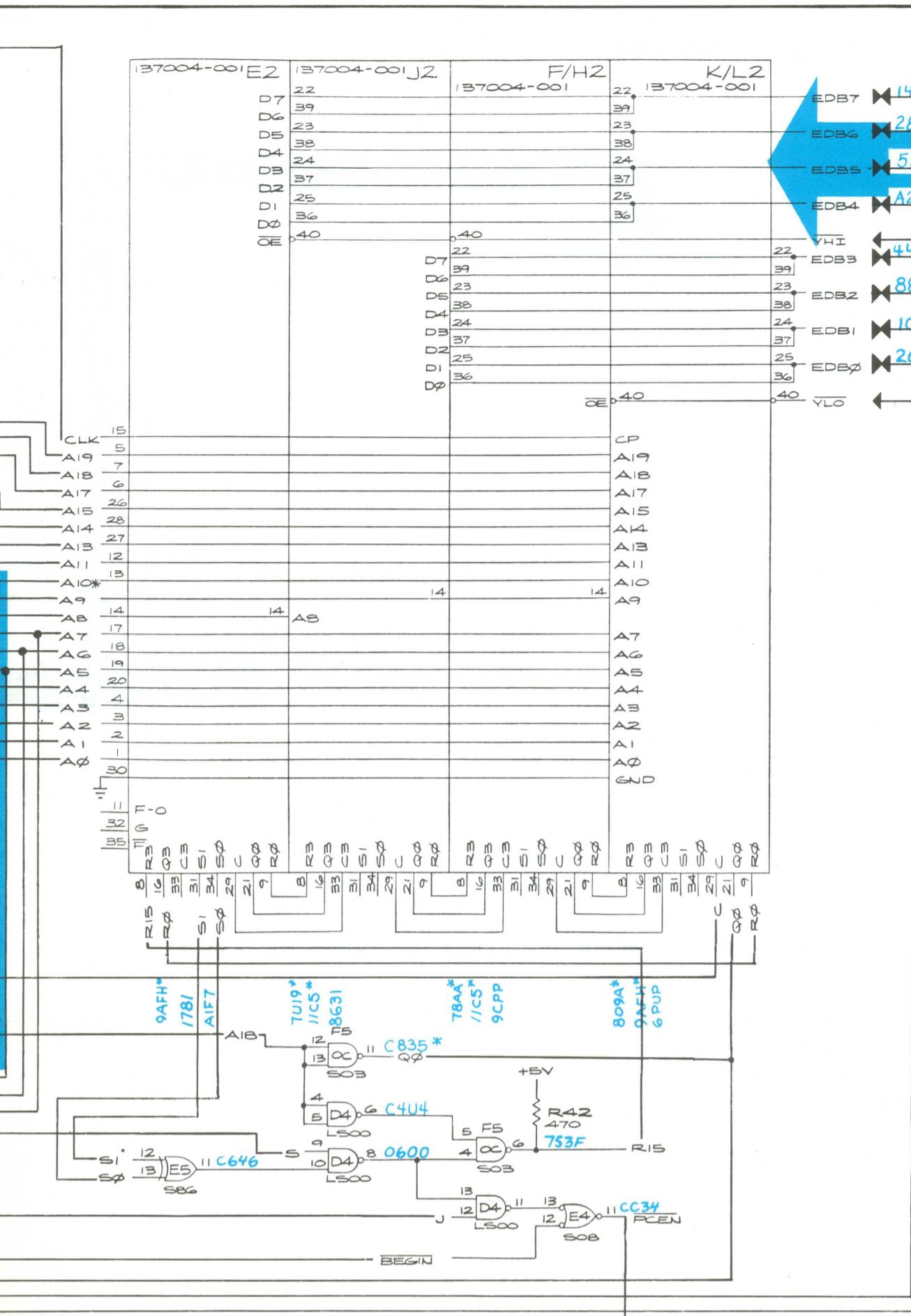
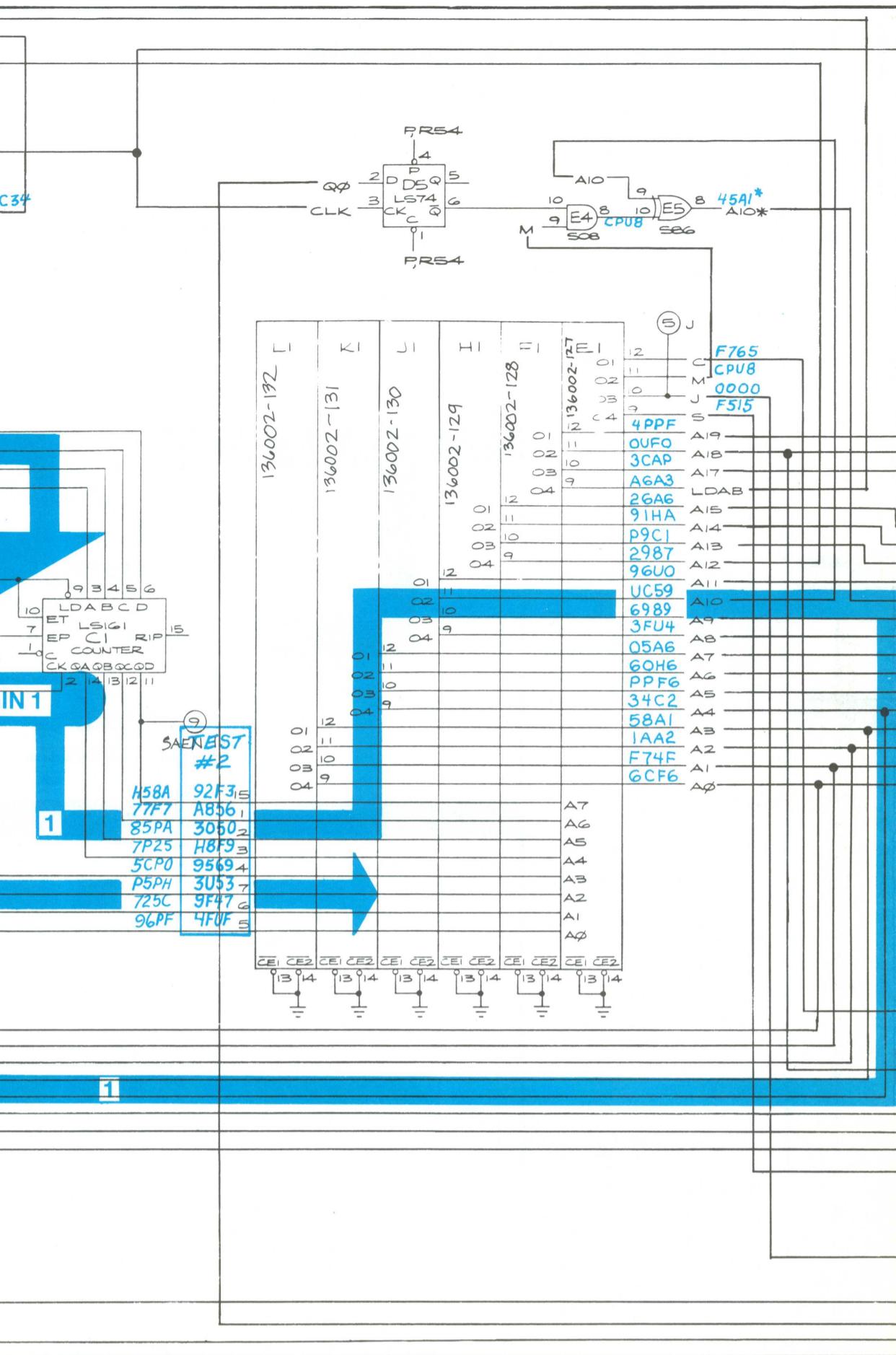
1



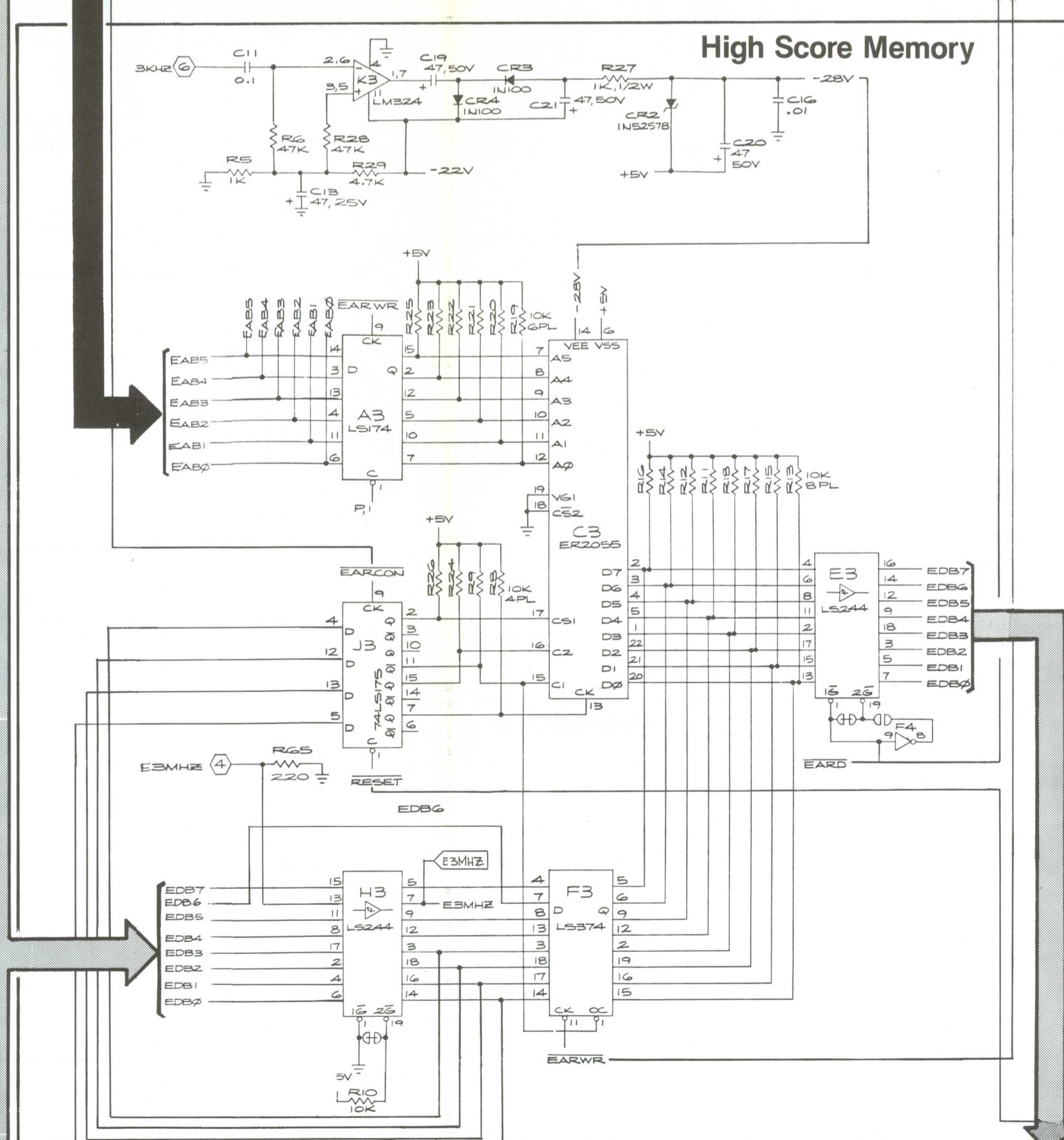
Blue arrows indicate signal flow of each test during signature analysis.



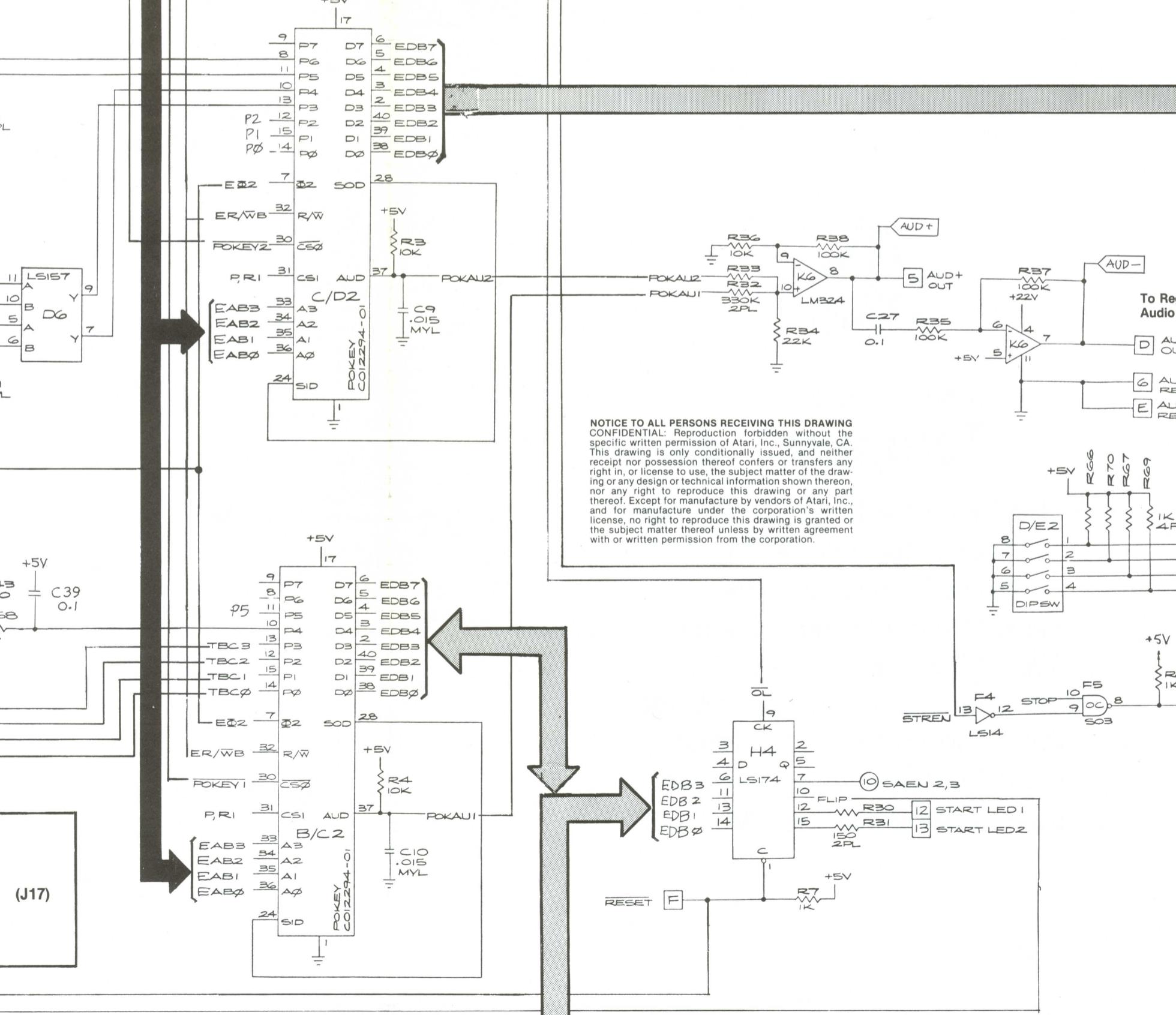
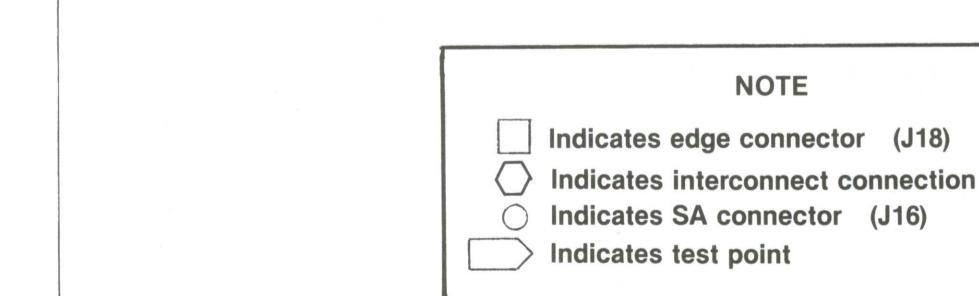
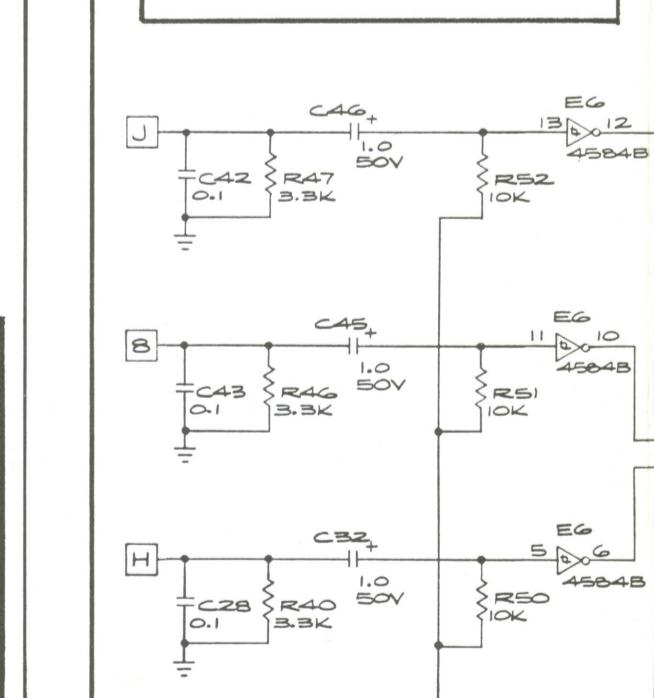
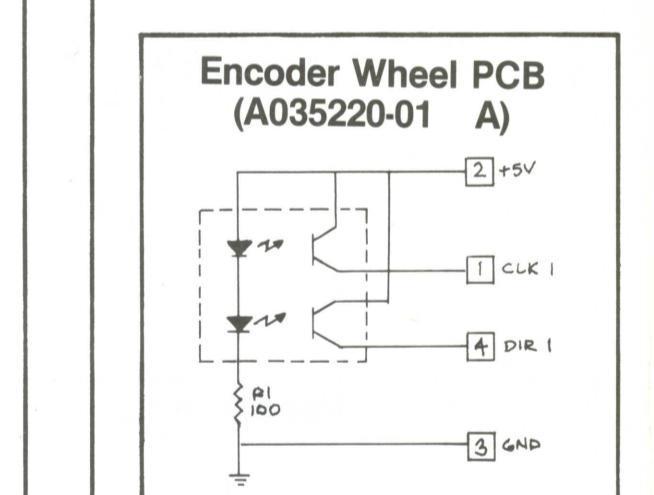
Math Box



High Score Memory



Player Inputs and Audio Output



NOTE

- Indicates edge connector (J18)
- Indicates interconnect connection (J17)
- Indicates SA connector (J16)
- Indicates test point

ORIGIN

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